

JCE10 U.S. PRO  
09/649827

	Subclass	Class

ISSUE CLASSIFICATION

**PATENT NUMBER**

**U.S. UTILITY Patent Application**

JCJ  
JC 826

O.I.P.E.	PATENT DATE
SCANNED <u>NSF</u> Q.A. <u>[Signature]</u>	

APPLICATION NO. 09/649827	CONT/PRIOR D	CLASS 156	SUBCLASS 333	ART UNIT 1733	EXAMINER <u>Krabile</u>
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SANTS Edward Schrock Tongbi Jiang 3089

## Method for attaching a semiconductor die to a substrate

TITLE

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PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)	(Date)			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____ _____ _____				ISSUE FEE	
	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
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